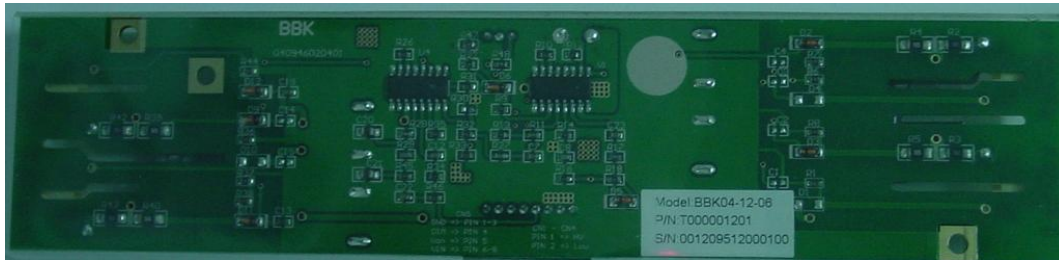


Piezo Ceramics DC-AC Inverter

Specification



Note: Actual product image may be different.

Model	BBK04-12-06
Part No.	T0-475B0
Description	4 lamps with wide range dimming control
Customer	
Hardware Rev	1.0
Document Rev	1.0

Approved by	Verified by	Prepared by
Bart	Bart	Catherine

Revision Record

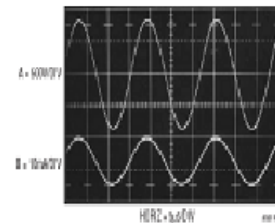
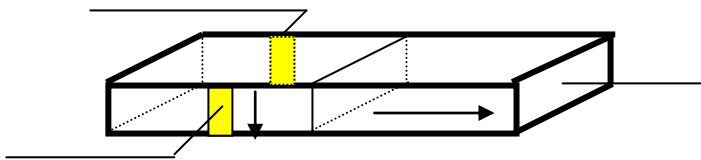
Request Document No.	Date	Page	Item	Description	Revision
TCTT12092201	22/12/2009				1.0

1. General:

Piezoelectric ceramics are used to convert electric energy to mechanical energy and vice versa. Piezoelectric transformer can generate a high voltage output by a low voltage input through the utilization of mechanical resonance and magnification phenomenon of the piezoelectric transducer.

1.1 Principle:

The piezoelectric transformer has primary and secondary electrodes on the piezoelectric ceramics. The primary side is polarized in the thickness direction and secondary side is polarized in the length direction. When a voltage with the resonance frequency is applied on the primary side, a strong mechanical vibration is generated by “inverse piezoelectric effect” of the ceramics, and a high voltage is output from the secondary side, matching its vibration frequency by “direct piezoelectric effect”.



1.2 Advantages & special features:

- No EMI (Piezo ceramics)
 - > 85% High efficiency
 - Inflammability (no liability)
 - Wide range no flicker dimming
 - One size fits all
 - Constant current mode
 - Wide operating temperature -40°C to $+85^{\circ}\text{C}$
 - Independent open lamp protection
 - Independent short circuit protection
 - Balanced sine wave output, no harmonic current noise
 - Balanced sine wave output, ultra low harmonic current noise
 - Short start up time, extended CCFL lifespan
 - Open lamp and short circuit protection
 - Built-in Arc Protection
 - 100% full load test
 - Compact Size, high reliability
 - Low heat generation
 - RoHS compliant with Piezo ceramic exemption
- *Spec subject to models

2. Detailed specification:

2.1 Electrical characteristics (Ta=25±5°C)

	ITEM			TEST	Min.	Typ.	Max.	Unit	Notes
1	Input voltage		Vin	-	10.8	12	13.2	v	
2	Input current	Min	Iin	D _{PWM} =20% & V _{IPWM} 5V	-	0.1	-	A	
		Max		D _{PWM} =100% & V _{IPWM} =0V	-	2.65	-		
3	Input Inrush Current		-	D _{PWM} =100% & V _{IPWM} =0V	-	-	5	A _{peak}	Initial power on only.
4	Output Inrush Current		-	D _{PWM} =50%	-	-	10	mA _{peak}	
5	Total Output Current		Iout	D _{PWM} =100% & V _{IPWM} =0V	20	24	28	mA	
6	Individual Output Current	Min	I _L	D _{PWM} =20% & V _{IPWM} =5V	≥ 2.1	≤ 2.6	≤ 3.1	mA	Low volt side of LOAD
		Max		D _{PWM} =100% & V _{IPWM} =0V	7.0	7.5	8.0		
7	Oscillating Frequency		Fw	-	50	53	60	KHz	
8	Minimum Duty Ratio		Dmin	D _{PWM} =D _{min}	-	-	-	%	
9	On/Off Control Voltage	On	V _{B_{LON}}	-	2.6	-	5	v	
		Off		-	0	-	2.5	v	
10	On/Off Control Current		I _{B_{LON}}	-	5	-	10	mA	
11	Internal DC Control Voltage	Min	V _{IPWM}	-	-	5	-	v	Duty Ratio ≤ 1%
		Max		-	-	0	-	v	Duty Ratio=100%
12	Internal DC Control Current		I _{IPWM}	-	-	5	-	mA	
13	Open Lamp Voltage		Vs	Ta=-20°C	TBD	-	3500	V _{rms}	Under B/L condition
				Ta=0°C	2820	-	3500		
				Ta=25°C	2090	-	3500		
14	Lamp Voltage		Vw	-	650	750	800	V _{rms}	*120KΩ load
15	DC Bias Level		I _{DC}	-	-	-	10	%	$\frac{I_{peak} - I_{-peak}}{I_L}$

Note: Lamp voltages are measured with a simulated resistive load. Piezo inverter will automatically adjust output voltage to compensate for load changes caused by lamp manufacturing tolerance, ambient temperature, lamp aging and etc.

2.2 Environmental Characteristic:

Storage Temperature : -10°C ~ +85°C Operating Temperature: -10°C ~ +85°C
 Storage Humidity : 90% Max. Operating Humidity : 90% Max
 (Non-condensation)

2.3 Protection Mechanism:

Condition	Test Method	Protection	NOTE
Over Voltage	All Open Lamp	Shutdown	0.1S < T _{fault} < 2S
Over Current	Input Shutter	Shutdown	
Output Short	Output Shutter: 2K	Low Voltage (30V~50v)	
Arcing	Transformer Open	Shutdown	

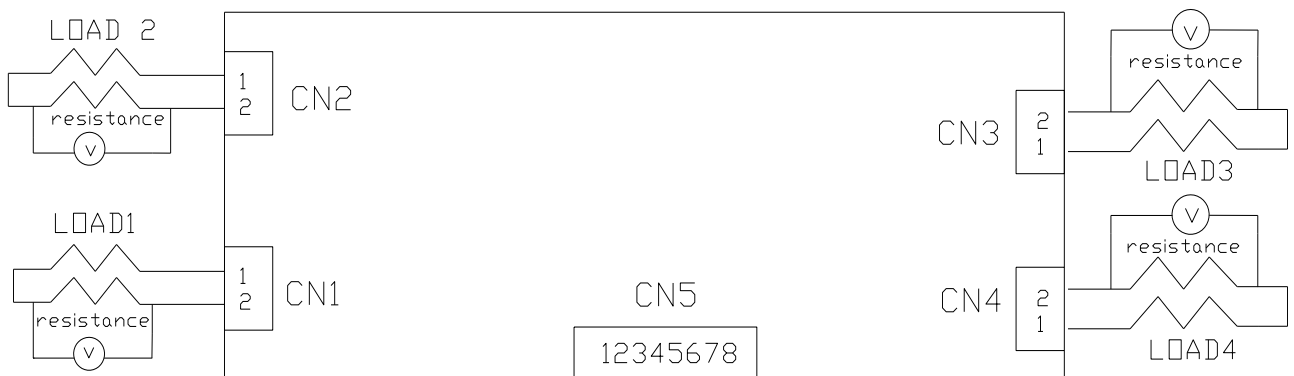
3. Application Notes:

- 3.1 Always connect output loading before turning on the unit to avoid damages.
- 3.2 Avoid over stressing the high voltage output connector by using short wire.
- 3.3 Avoid bending, twisting or applying any pressure to the PCB and Piezo transformer.

4. Typical Application:

LCD-TV, LCD-Monitor CCFL backlight

4.1 Input connector pin assignment:



Input: CN5 [JST S8B-PH-KL]

Pin	Signal name	Function
1,2,3	GND	GND
4	VADJ	0 to +5V voltage level
5	ON/OFF	+5V/0V
6,7,8	VIN	+12V

Voltage level dimming: 0V Brightest, +5.0V Dark

4.2 Output connector pin assignment:**Output: CN1,CN2,CN3,CN4 [JST SM02(8.0)B-BHS-1-TB]**

Pin	Signal name	Function
1	CFL HOT	CFL High voltage
2	CFL COLD	CFL Low Voltage

*Wrong connections will cause electric shock and also break down of the product.

5. Reliability**5.1 Production tests**

	Test item	Test condition	Criteria
1	Low temp. Operation	Ta=-20°C 500 hr.	Measurement must be performed 1 hr. after taken out from the chamber. Must meet initial performance except CCFL deterioration.
2	High temp. Operation	Ta=+85°C 500 hr.	
3	High temp & Humidity	Ta=50°C, 80%RH, 1000 hr.	
4	Low temp. Storage	Ta=-40°C, 240hr. Non operation	Measurement must be performed 4 hr. after taking out from the chamber. Must meet initial performance except CCFL deterioration.
5	High temp. Storage	Ta=+85°C, 240hr. Non operation	
6	Thermal shock	Ta=-20°C, 30Min. <->+60°C, 30Min. 200 cycles, non operation, Transition duration less than 3 min.	Measurement must be performed 4 hr. after taking out from the chamber. Must meet initial performance except CCFL deterioration.
7	On-Off Cycle	Ta=25±3°C 500 hr, 10[s] ON, 10 [s] OFF 100,000 cycles	Must meet Initial performance except CCFL deterioration.
8	Shock	50G 11mSec. Half-sinusoidal waveform X, Y, Z, 6 directions	No noticeable changes allowed
9	Vibration	10-55Hz, 10 min. 1.0G X, Y, Z 3 directions, 1 time	No noticeable changes allowed

6. Safety requirements

6.1 Applicable safety standards

- UL 60950-1:2003
- CSA C22.2 NO.60950-1-03
- IEC60950-1:2001
- EN 60950-1:2001

6.2 Abnormal tests

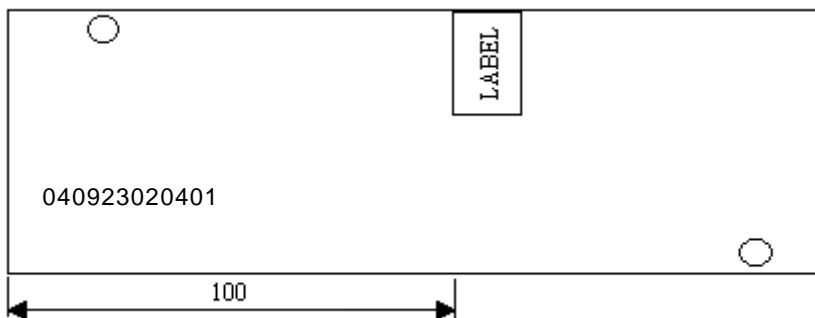
There must be no smell, smoke and fire cause by any failure on the inverter circuit. All components open/short test must be performed and reported. Especially, should not rely on a fuse or an over load protection function of the power supply.

6.3 Limited current circuit

The inverter conforms to IEC60950 limited current circuit spec and is UL

7. Additional notes:

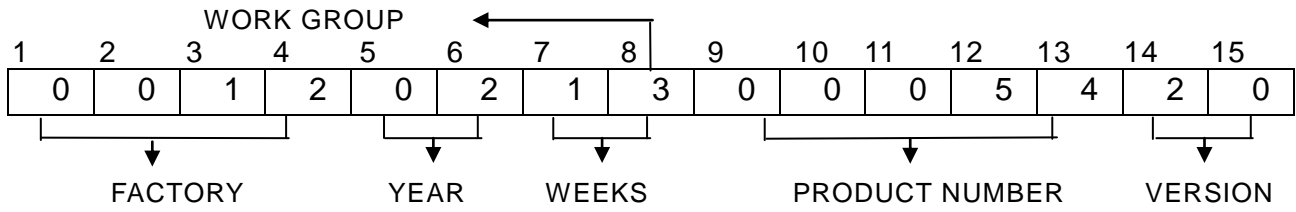
7.1 Label position (Reverse side of PCB)



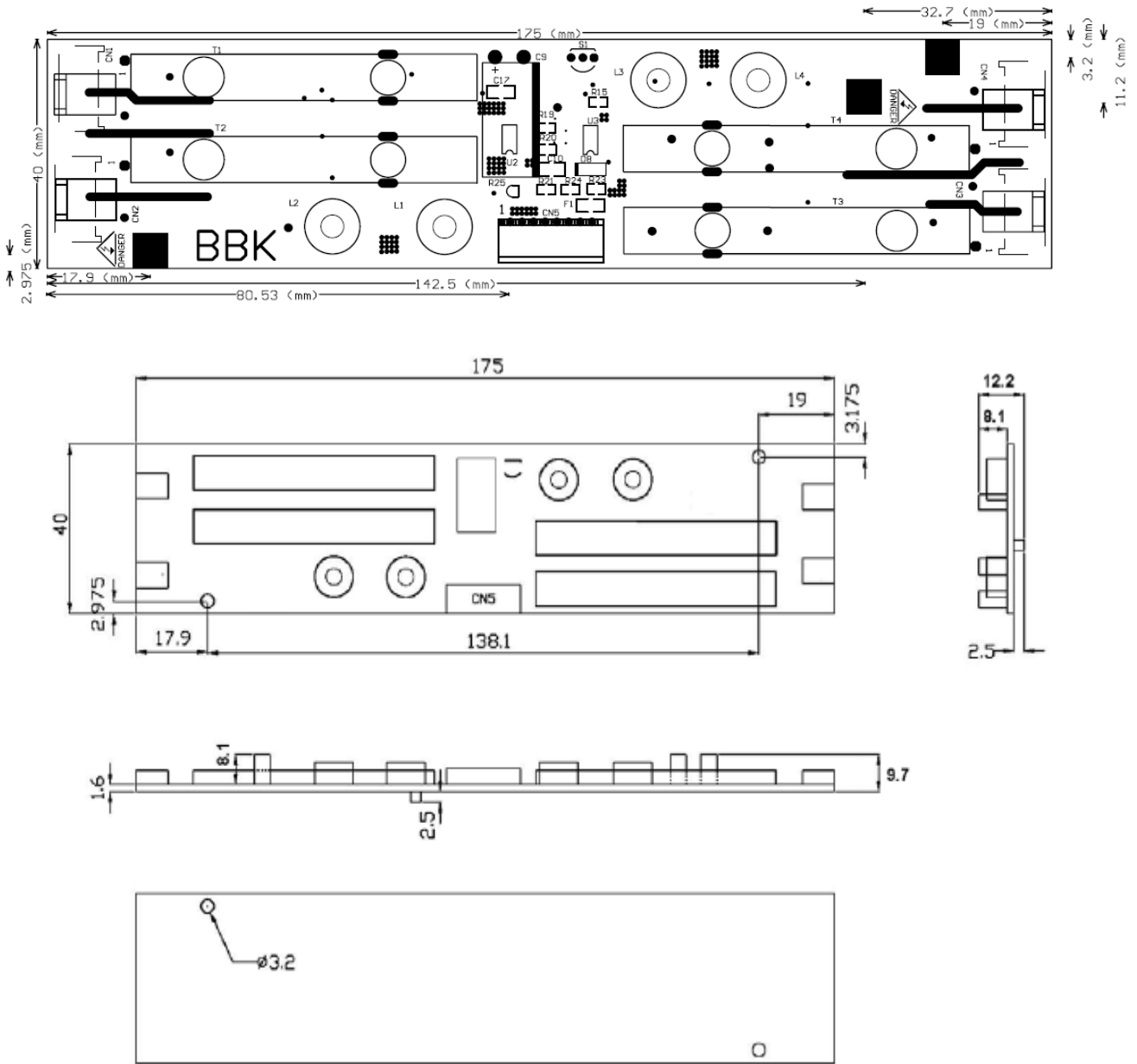
Label: Includes model, part number and data code.

Model
Part No
Data code

7.2 Data code (example):



8. PCB layout:



NOTES:

1. PCB Board general tolerance: $\pm 0.1\text{mm}$
2. Hole tolerance: $\pm 0.1\text{mm}$
3. Base board thickness: 1.6mm
4. Material: FR-4
5. Thru hole: 3.2mm